

FIG. 1

ADDRESS SPACE
OF CONTROL
DEVICE 2

MEMORY SPACE

LOGICAL SPACE

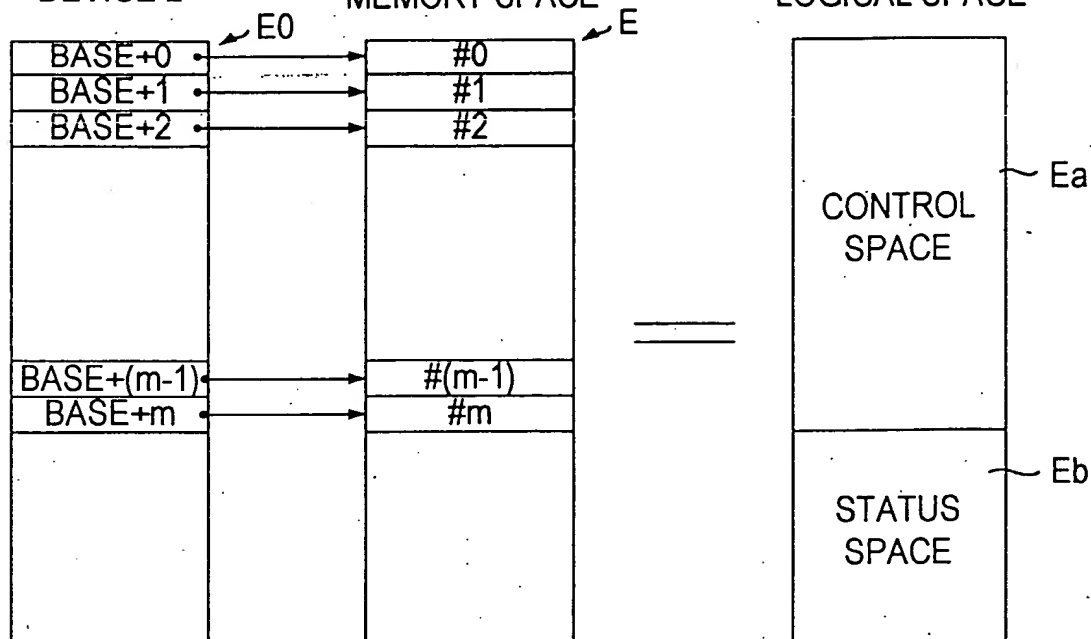


FIG. 2

LOGICAL SPACE

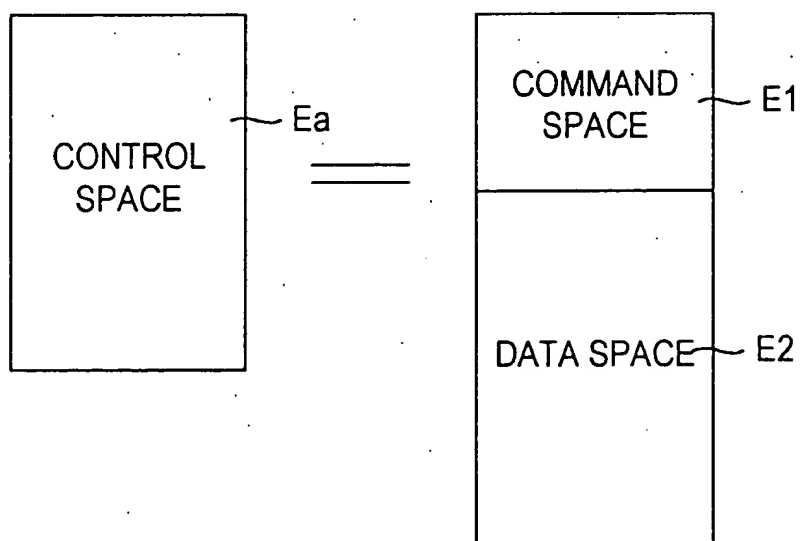


FIG. 3

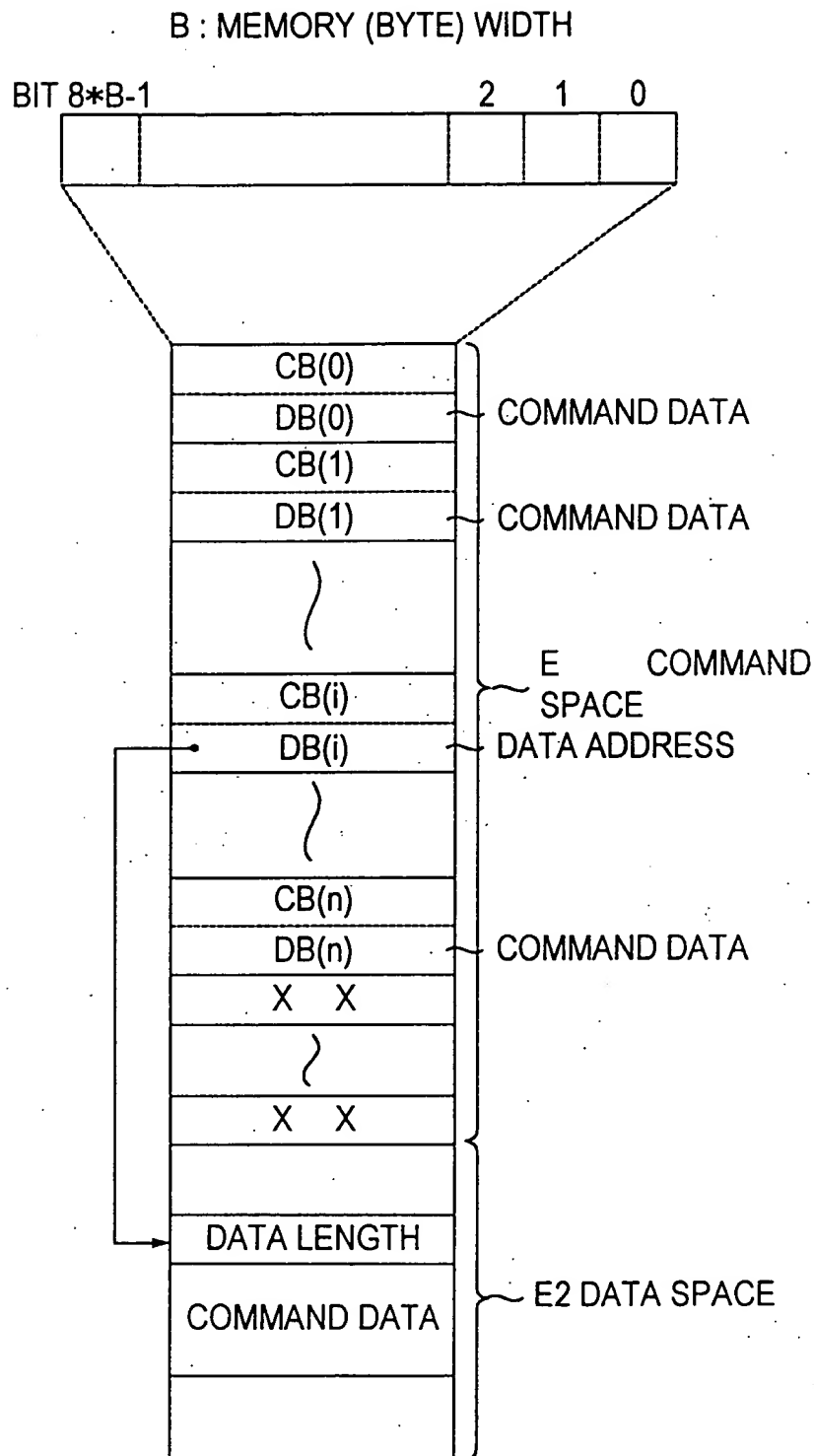


FIG. 4

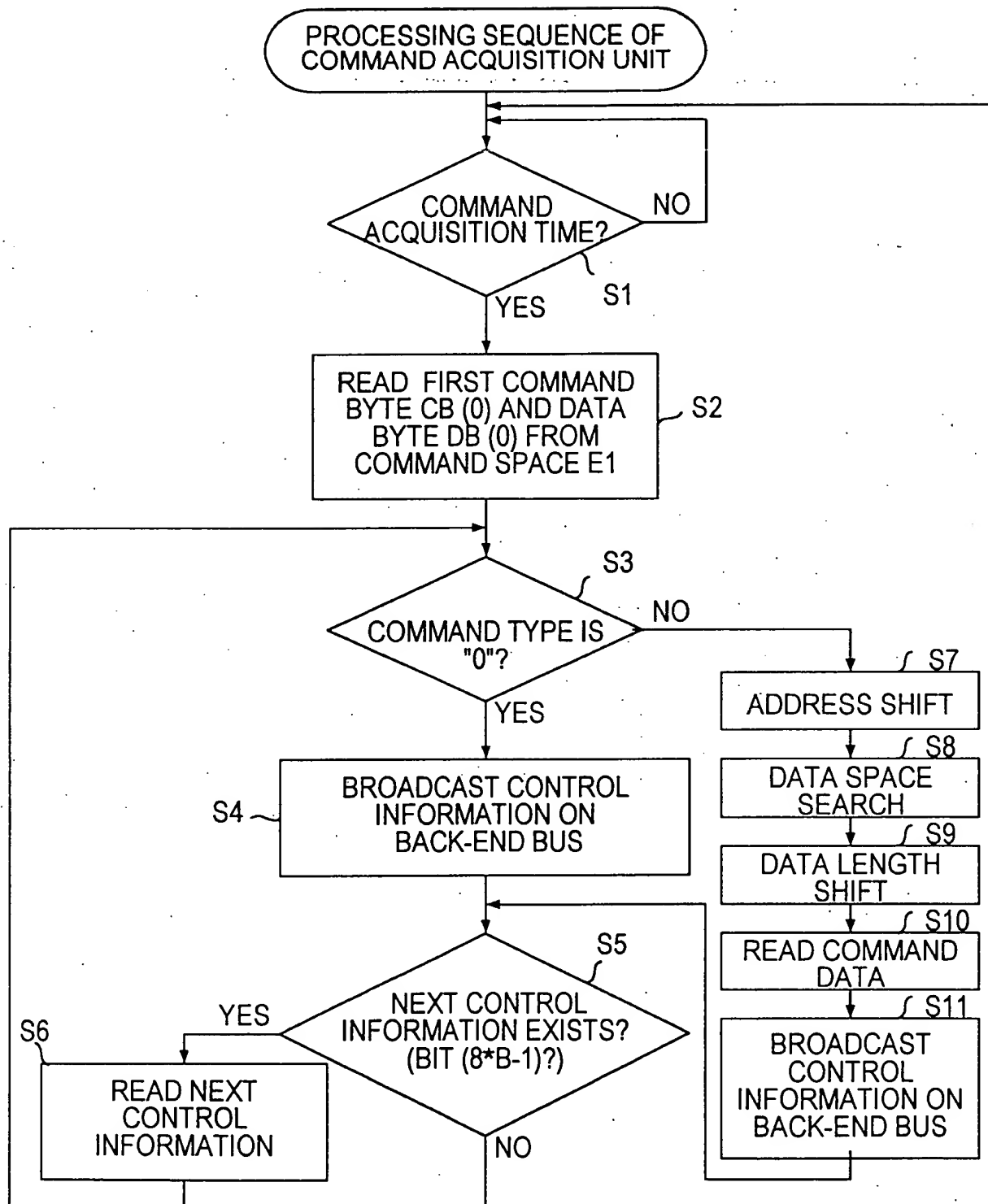


FIG. 5

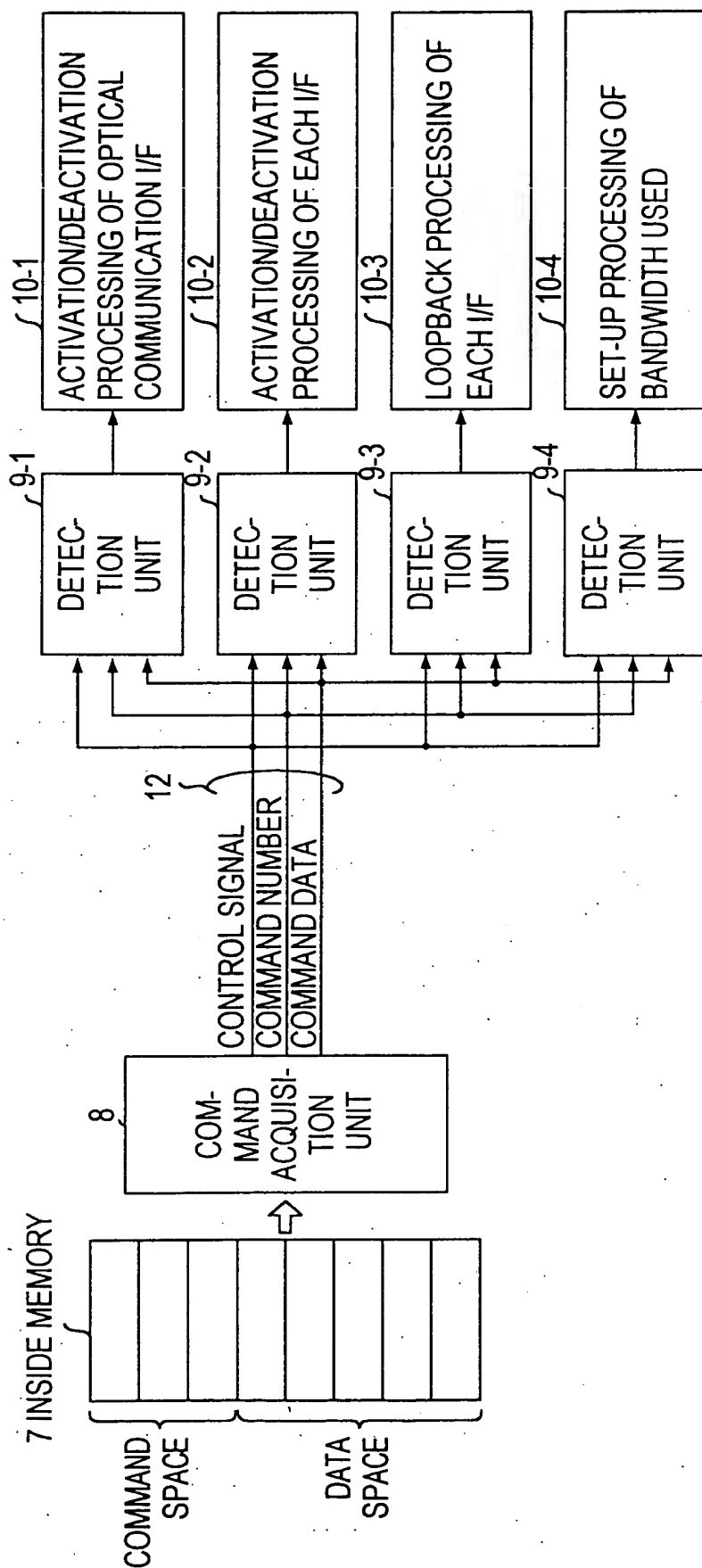


FIG. 6

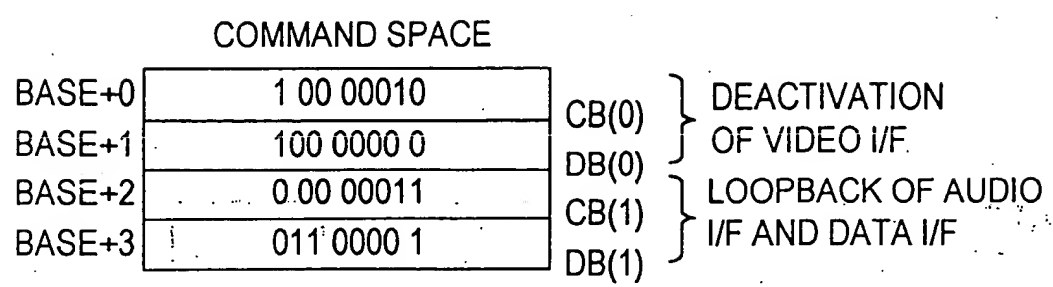


FIG. 7(a)

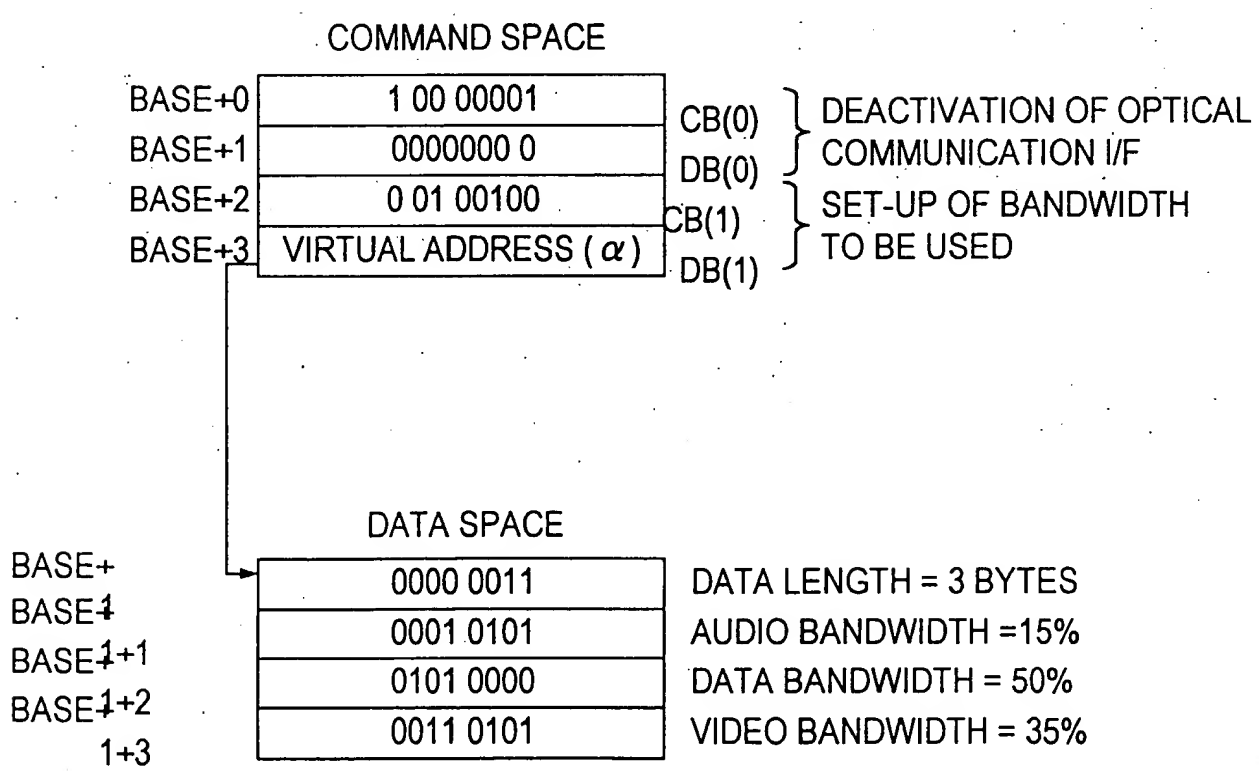


FIG. 7(b)

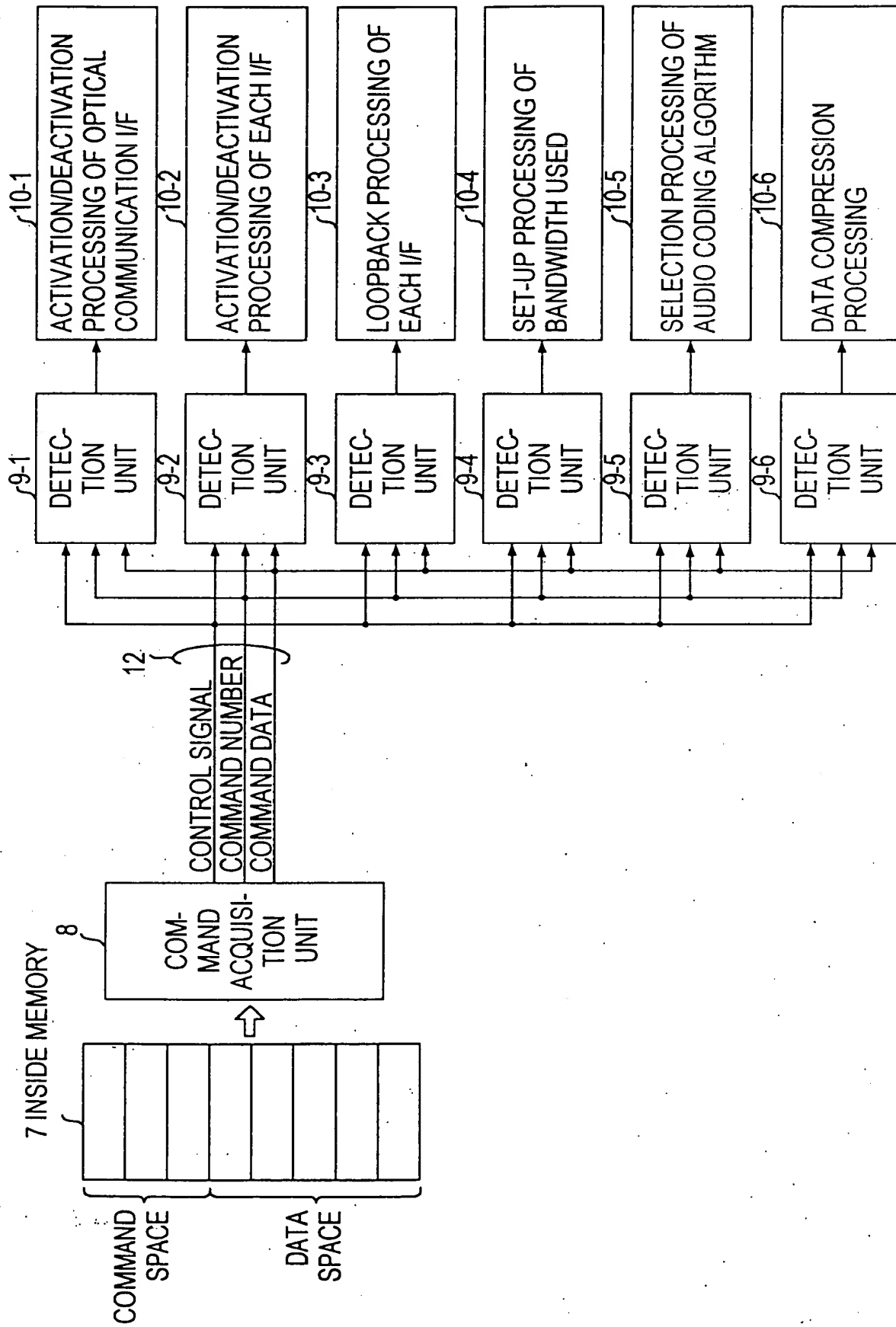


FIG. 8

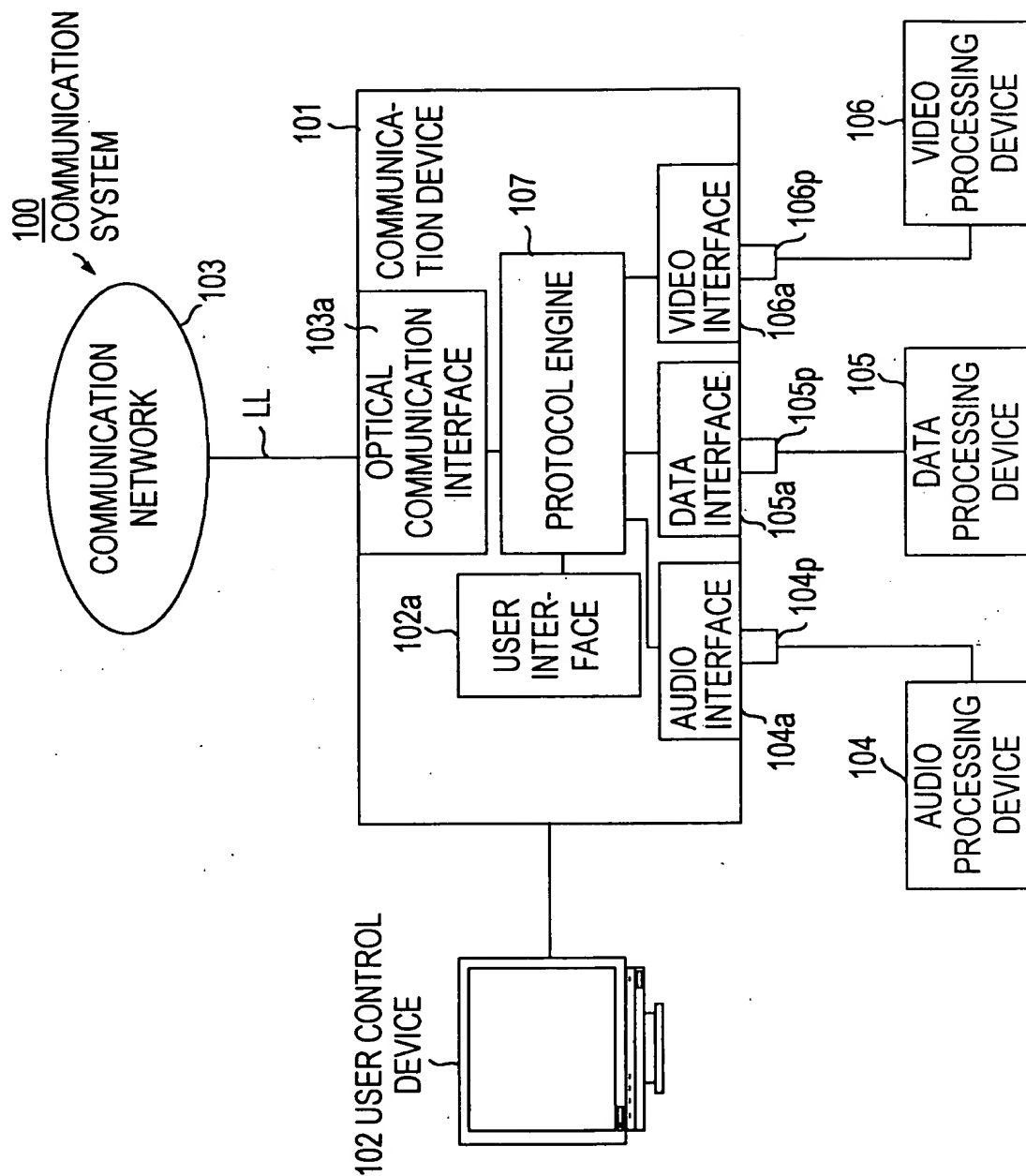


FIG. 9

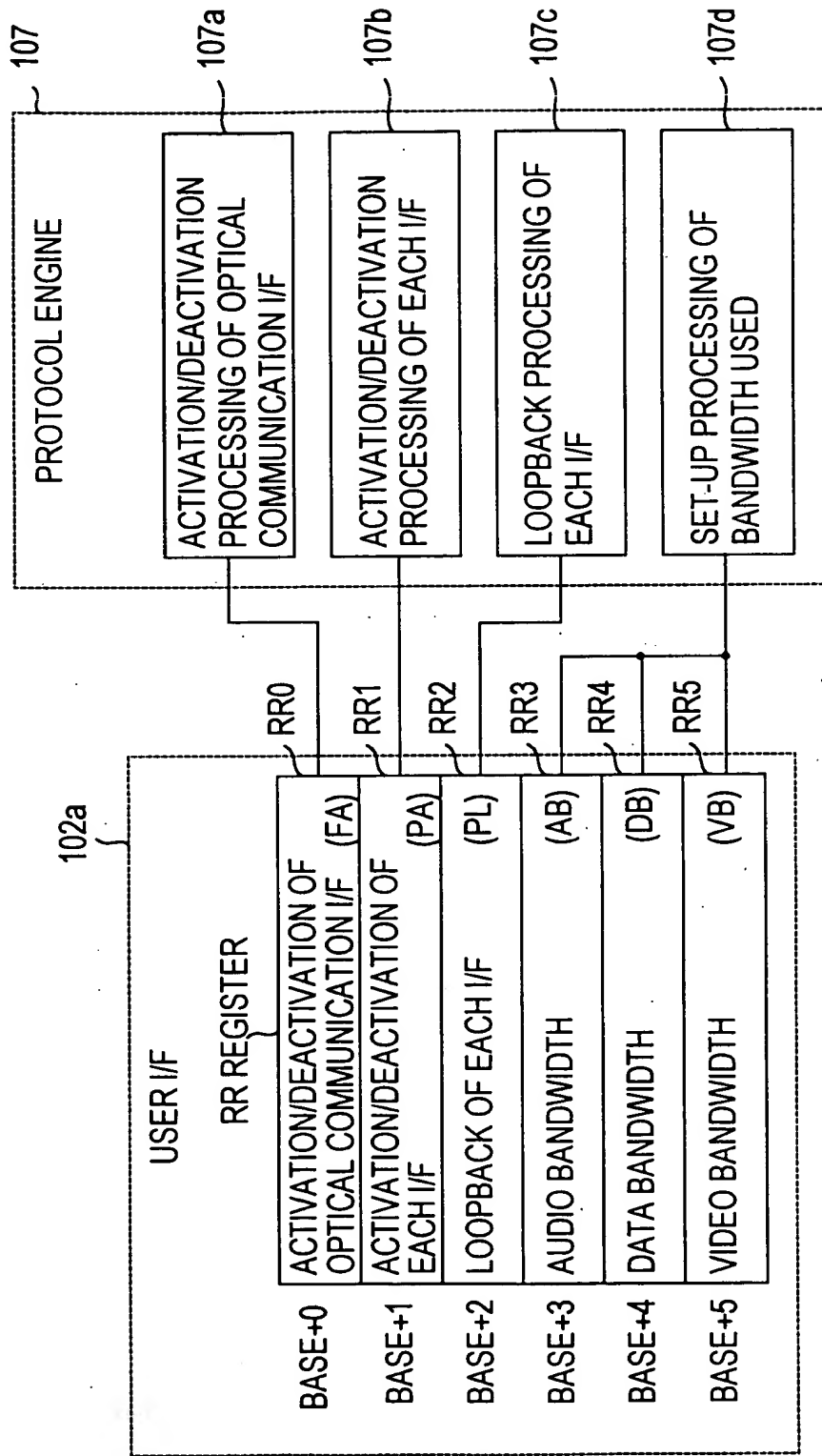


FIG. 10

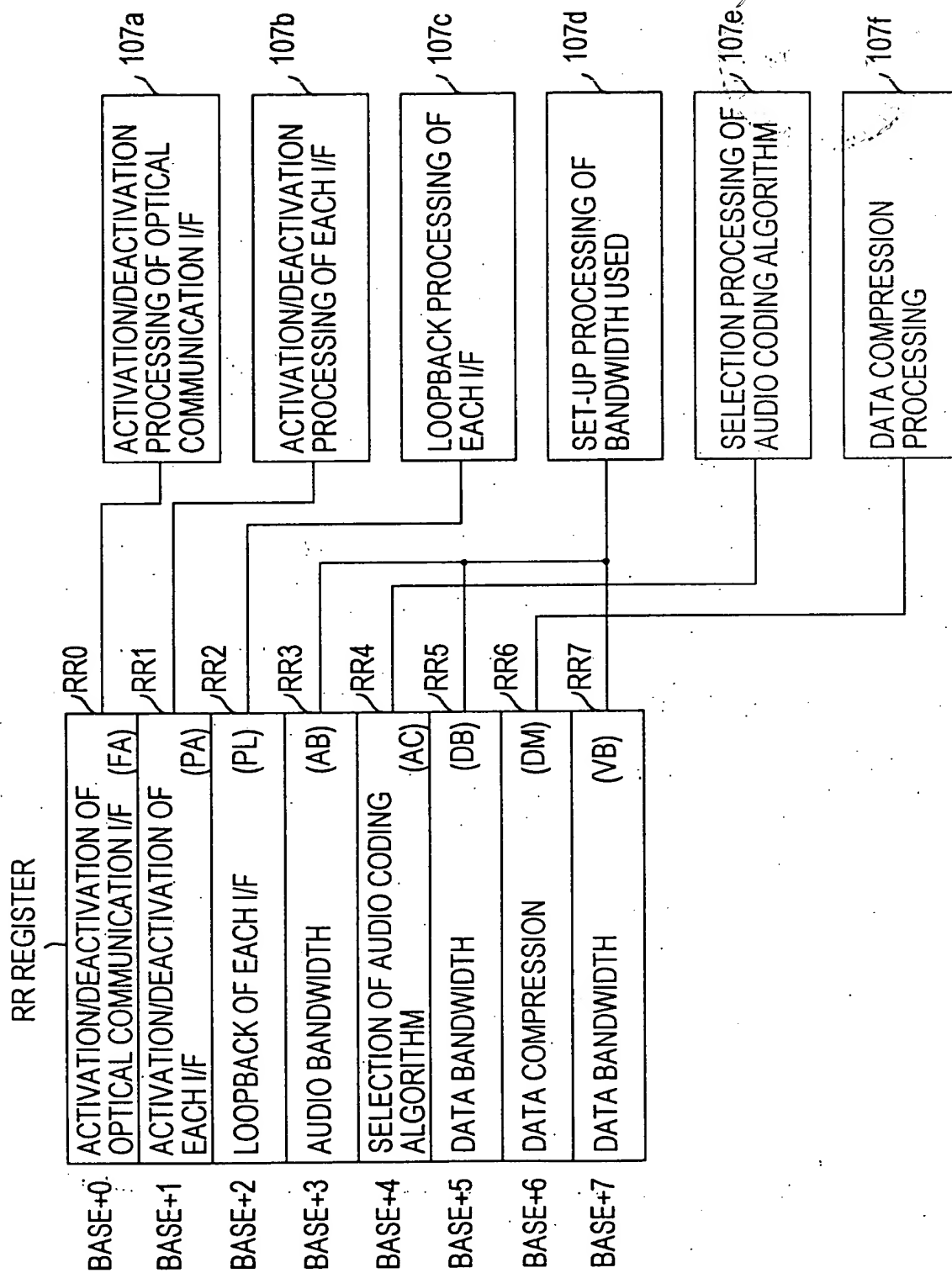


FIG. 11

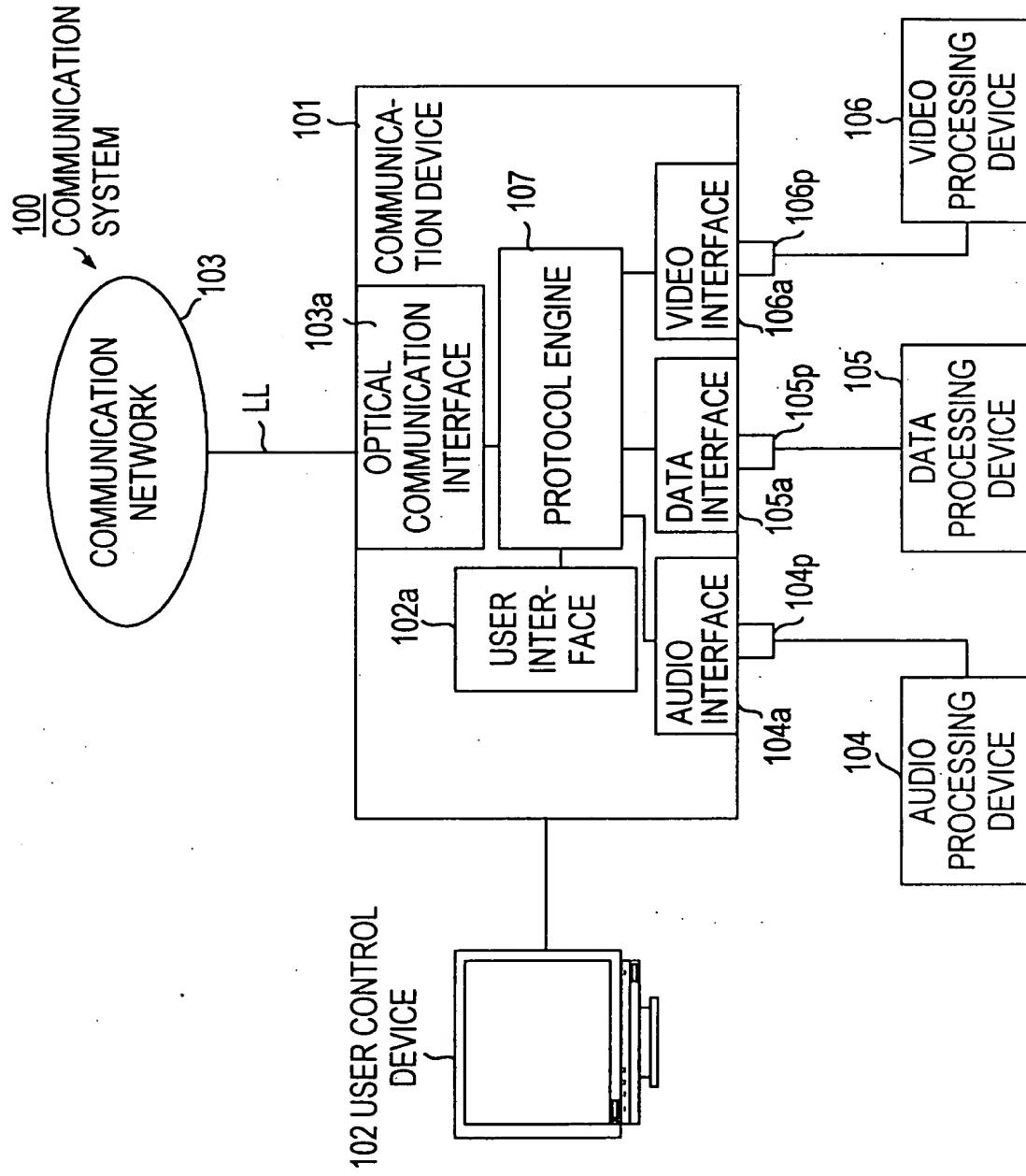


FIG. 9 (PRIOR ART)

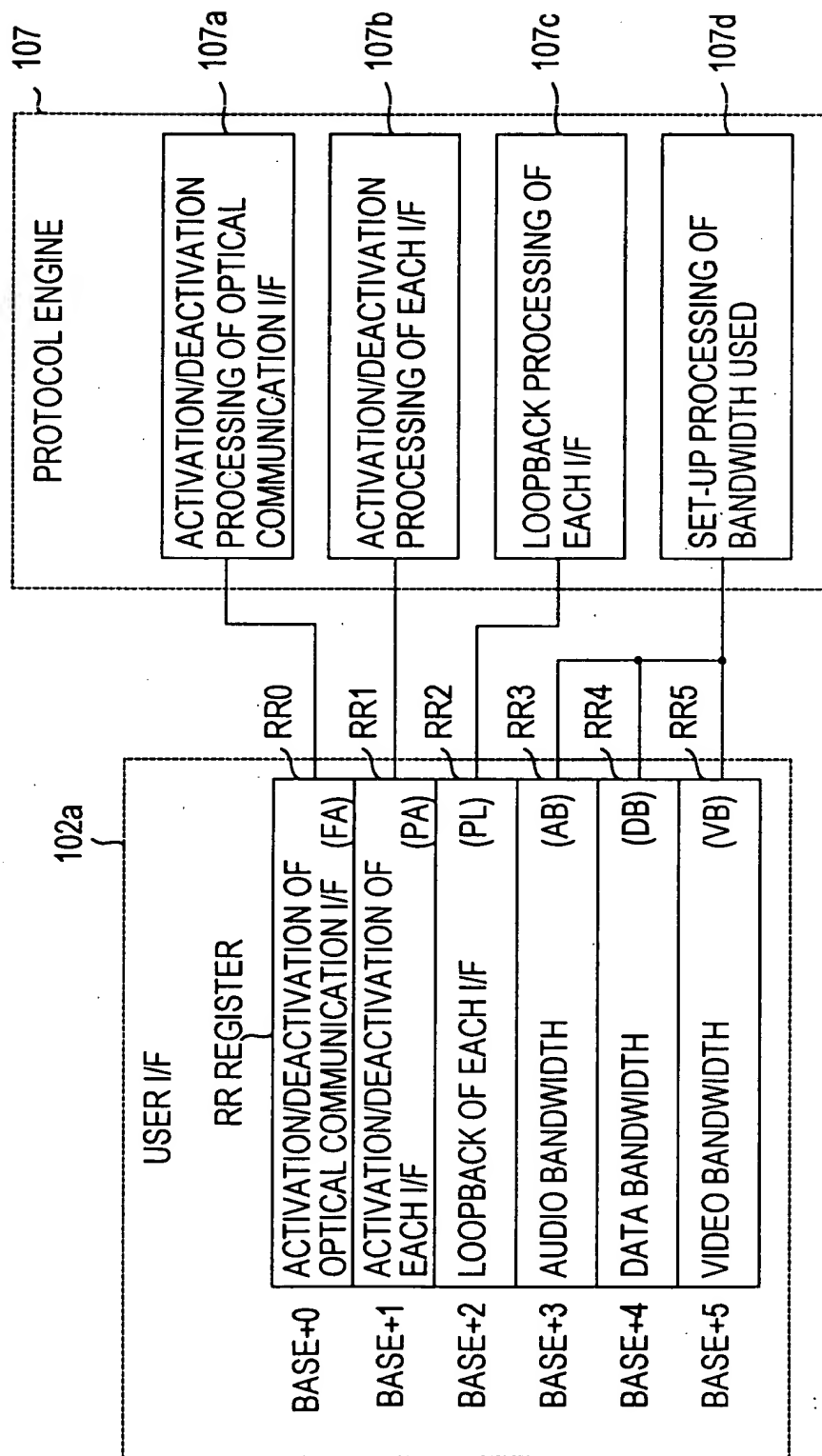


FIG. 10 (PRIOR ART)

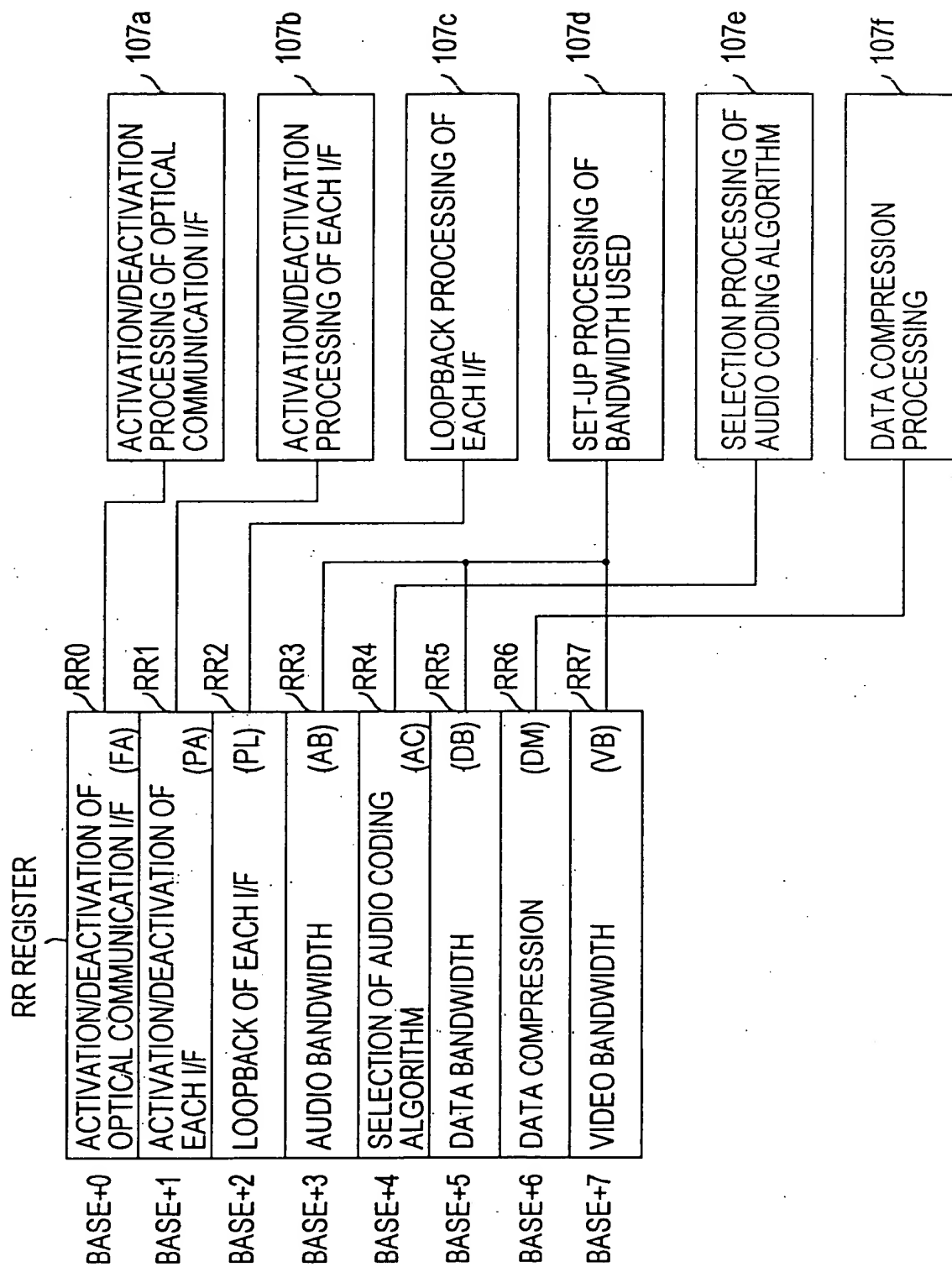


FIG. 11 (PRIOR ART)